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**Application
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M844	2004-03-12	10	Y <input checked="" type="checkbox"/>	2006-12-14 13:41:59.0	jtorres1

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Continuity Information for 10/665080

Parent Data

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2002-66134	10/29/2002	REPUBLIC OF KOREA

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Inventor Information for 10/665080

Inventor Name	City	State/Country
JANG, KYUNG-OUN	INCHEON-CITY	KOREA, REPUBLIC OF
KIM, EUNG-SUEN	INCHEON-CITY	KOREA, REPUBLIC OF

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Inventor Name Search Result

Your Search was:

Last Name = JANG

First Name = KYUNG-OUN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09643534	6381151	150	08/22/2000	HIGH EFFICIENCY SWITCHING CONTROLLER	JANG, KYUNG-OUN
09761902	6507080	150	01/17/2001	MOS TRANSISTOR AND FABRICATION METHOD THEREOF	JANG, KYUNG-OUN
10300293	6853040	150	11/20/2002	MOS TRANSISTOR AND FABRICATION METHOD THEREOF	JANG, KYUNG-OUN
10665080	Not Issued	30	09/16/2003	EMI cancellation method and system	JANG, KYUNG-OUN
10698056	6972971	150	10/29/2003	PULSE WIDTH MODULATION SIGNAL GENERATOR AND SWITCHING MODE POWER SUPPLY INCLUDING THE SAME	JANG, KYUNG-OUN

Inventor Search Completed: No Records to Display.

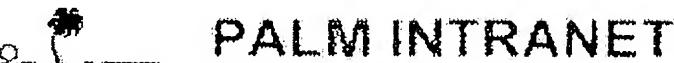
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Correspondence Address for 10/665080

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